

AMERICA'S CHIP RESURGENCE

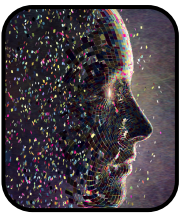
Building U.S. Strength through Investments in Domestic Chip Research, Design, and Manufacturing

To be the world's economic and technology leader, America must lead the world in semiconductors.

U.S. semiconductor leadership strengthens our economy, national security, and global competitiveness. To achieve these goals, the Administration and Congress must accelerate policies to enhance U.S. capabilities in semiconductor research, design, and manufacturing.

U.S. Semiconductor Leadership – A National Priority

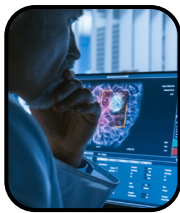
- **Economic Strength:** Revitalizing domestic manufacturing and expanding the U.S. chip design ecosystem to strengthen supply chains, promote innovation, and create jobs.
- **National Security:** Bolstering the defense industrial base and protecting critical infrastructure.
- **Technology Leadership:** Out-innovating competitors in AI, quantum, and industries of the future.



AI



Defense



Healthcare



5G/6G



Transportation



Energy



Agriculture

Critical Investments in U.S. Chip Manufacturing and Innovation

Originally conceived and authorized during the first Trump Administration, the CHIPS Act was enacted to address national security risks and supply chain vulnerabilities facing the U.S. The Act continues to address pressing economic and national security priorities and rests on two pillars:

- 1) **Incentives for manufacturing** in the form of a 25% investment tax credit and \$39 billion in grants
- 2) **Investments in chip innovation** through \$13 billion for research programs and infrastructure

These incentives and investments are delivering results for America:

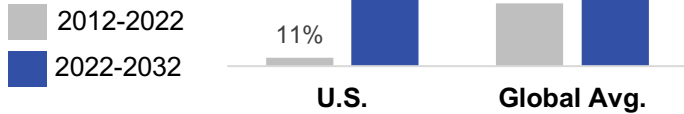
- Manufacturing incentives have sparked \$450 billion in private sector investments across the U.S. chip supply chain – logic, memory, analog, advanced packaging, equipment, and materials – creating over 50,000 manufacturing jobs and 80,000 construction jobs, which will support hundreds of thousands of additional jobs throughout the economy.
- R&D investments are building the framework to maintain and extend U.S. technology leadership, with benefits that will multiply throughout the economy and enhance our national security.
- Workforce development initiatives in partnership between companies, community colleges, and universities are training future semiconductor technicians, chip designers, and engineers.

Actions to protect the U.S. economy or national security (e.g., tariffs, export controls, etc.) should be coupled with incentives and investments in research, design, and manufacturing to boost U.S. leadership and competitiveness.

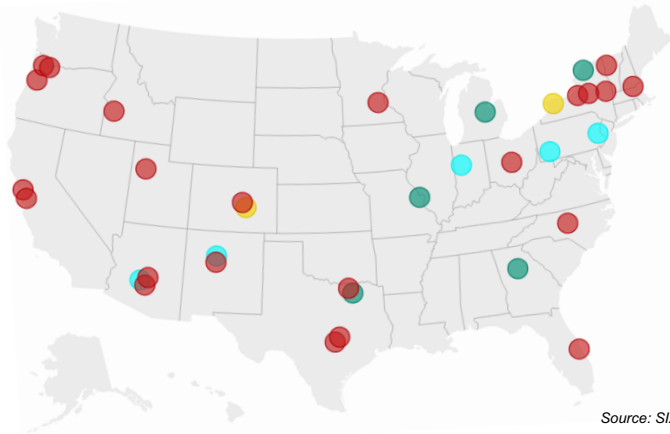
Manufacturing Investments

In the decade prior to the CHIPS Act, U.S. chipmaking capacity grew only 11%. From 2022-2032, however, U.S. capacity is projected to increase 203%, the highest growth rate in the world, with a substantial increase in advanced logic, memory, analog, and advanced packaging.

Fab Capacity Increase by Location



● Semiconductors ● Packaging ● Equipment ● Materials



Research Investments

Establishing the R&D infrastructure for long-term U.S. chip innovation and design leadership, closing the gap from “lab to fab,” and growing the semiconductor workforce and talent pipeline.

U.S. NATIONAL SEMICONDUCTOR TECHNOLOGY CENTER

PACKAGING CHIPS for AMERICA

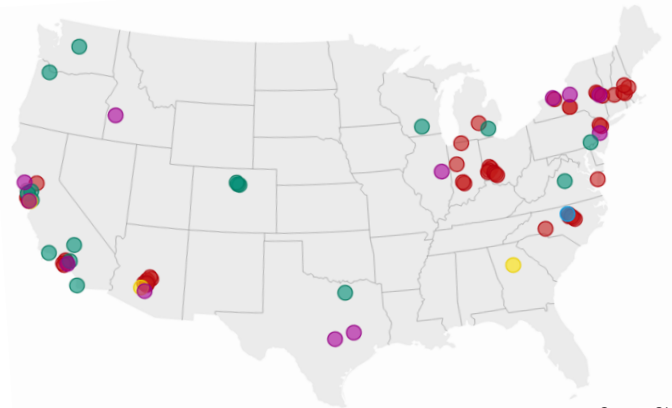
ManufacturingUSA



MICROELECTRONICS COMMONS

CHIPS for AMERICA METROLOGY

● DOD Microelectronics Commons ● NSTC ● NAPMP ● Metrology ● Manufacturing USA



Future Policies to Strengthen America

As the Administration and Congress consider measures to protect the U.S. economy and national security (e.g., export controls, tariffs, etc.), a strategy for U.S. semiconductor leadership must consist of robust, positive measures to promote U.S. semiconductor innovation and competitiveness.

- 1. Strengthen Tax Credits for Manufacturing and Design.** The existing, highly impactful tax credit (IRC §48D) should be **extended** beyond 2026 to incentivize continued domestic manufacturing and **expanded** to include chip design and R&D to level the playing field with global competitors who offer targeted semiconductor R&D and design incentives.
- 2. Invest Ambitiously in Research and Innovation.** Existing CHIPS R&D programs should be accelerated and structured to align with industry priorities, and federal research agencies should be funded at authorized levels to ensure the U.S. leads in the technologies of tomorrow.
- 3. Grow the STEM Talent Pipeline.** The U.S. should make investments in U.S. STEM education and workforce development, as well as attract skilled semiconductor talent from around the world.

AMERICA MUST RUN FASTER TO WIN THE TECHNOLOGY RACE OF THE FUTURE